

claims currently pending in this application, including those not presently amended, have been reproduced below for the Examiner's convenience.

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1. (Amended) An electronic apparatus comprising:
a clock circuit that generates a clock signal having clock signal pulses generated at a predetermined cycle; and
a non-volatile memory circuit for counting clock signal pulses generated by said clock circuit and storing a count of the clock signal pulses.
2. An electronic apparatus according to Claim 1, wherein said non-volatile memory circuit comprises a ferroelectric memory.
3. An electronic apparatus according to Claim 1, further comprising a control circuit that controls said electronic apparatus,
wherein said control circuit controls said non-volatile memory circuit so as to count the clock signal pulses in response to the clock signal generated by said clock signal circuit.
4. An electronic apparatus according to Claim 1, wherein said electronic apparatus comprises a camera.

5. An electronic apparatus according to Claim 4, further comprising a control circuit that controls said electronic apparatus, and wherein said control circuit includes a central processing unit.

6. An electronic apparatus according to Claim 1, wherein, when a power supply battery for supplying power to said electronic apparatus is replaced, said non-volatile memory circuit starts counting in a state in which a predetermined value is added to the memory contents of said non-volatile memory circuit.

7. (Amended) An electronic apparatus comprising:
a time-keeping circuit that keeps time; and
a ferroelectric memory circuit that forms and stores a time signal concerning time kept by said time-keeping circuit.

8. An electronic apparatus according to Claim 7, further comprising a control circuit that controls the electronic apparatus,
wherein said control circuit controls said ferroelectric memory circuit so as to store the time signal for said time-keeping circuit in response to time-keeping by said time-keeping circuit.